

K.S.R.M. COLLEGE OF ENGINEERING (AUTONOMOUS)

(Approved by AICTE, New Delhi & Affiliated to JNTU ANANTHAPURAMU)

(Accredited by NAAC of UGC)

(An ISO 9001:2015 Certified Institution)

KADAPA – 516003 (A.P)

2020-2021

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING



CERTIFICATE

This is to certify that the Project Report entitled

Title	Design and Development of DC Micro Grid by Using PV Emulator for Load Flow Analysis
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is the bonafied record work done by

Name (Roll No.)		
M. ASLAM BAIG (189Y5A0231)	K. BALAJI (189Y5A0222)	S. KAMAL BASHA (189Y5A0252)
S. PAWAN SAI (189Y5A0254)	R. VENNELA (179Y1A0221)	


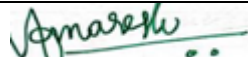
Department of Electrical & Electronics Engineering, K.S.R.M. College of Engineering (Autonomous), Kadapa and is submitted to JNTUA - ANANTAPURAMU in partial fulfilment of the requirements for the award of the degree of

BACHELOR of TECHNOLOGY

IN

Electrical & Electronics Engineering

During the year 2020-2021

	
Project Guide	Head of the Department
Name of the Faculty: Mr. P. Durga Prasad	Dr. K. AMARESH
Designation: Assistant Professor	Professor
Signature of External Examiner	