

K.S.R.M. COLLEGE OF ENGINEERING (AUTONOMOUS)

(Approved by AICTE, New Delhi & Affiliated to JNTU ANANTHAPURAMU)

(Accredited by NAAC of UGC)

(An ISO 9001:2015 Certified Institution)

KADAPA – 516003 (A.P)

2020-2021

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING



CERTIFICATE

This is to certify that the Project Report entitled

Title	Economic Load Dispatch Problem Using Genetic Algorithm
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is the bonafied record work done by

Name (Roll No.)		
K.LAKSHMIKANTH (179Y1A0212)	S.VENKATESH (179Y1A0222)	L.REVANTH REDDY (189Y5A0229)
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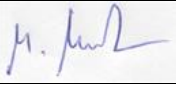
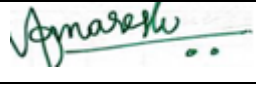
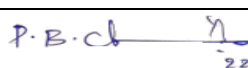
Department of Electrical & Electronics Engineering, K.S.R.M. College of Engineering (Autonomous), Kadapa and is submitted to JNTUA - ANANTAPURAMU in partial fulfilment of the requirements for the award of the degree of

BACHELOR of TECHNOLOGY

IN

Electrical & Electronics Engineering

During the year 2020-2021

	
Project Guide	Head of the Department
Name of the Faculty: Dr. M. Murali	Dr. K. AMARESH
Designation: Associate Professor	Professor
Signature of External Examiner	 22/7/2021