

**Name:** KONGANAPALLI DIVYA LAKSHMI

**Designation:** Assistant Professor

**D.O.B:** 14-05-1990

**D.O.J:** 03-07-2015

**Years of Experience:** 2 Years

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### **Experience Summary:**

- ❖ Working as Assistant Professor in the department of Electronics and Communication Engineering, KSRMCE, Kadapa since July 2015.

### **Academic Qualifications**

- ❖ Completed SSC from **pushpagiri High school, pulivendula, Kadapa dist.** in the year **2006** with **first class**.
- ❖ Completed Diploma from **Govt. polytechnic for women, Kadapa** in the year **2009** with **first class-distinction**.
- ❖ Awarded B. Tech. degree in **ECE** from **JNTUA University** in the year **2012** with **first class-distinction**.
- ❖ Awarded M. Tech. degree in **VLSI SYSTEM DESIGN** specialization from **JNTUA University** in the year **2014** with **first class-distinction**.

### **Project works guided**

- ❖ B. Tech Projects:- **02**

### **Subjects Taught under UG Level**

#### **UG Level:**

- VLSI design
- Radar systems
- Satellite Communication

## **Journals Published**

### **International**

[1] K.Divya lakshmi,M.Anand “*SECURE AUTHENTICATION THROUGH A NEAR FIELD COMMUNICATION IN ASYMMETRIC CRYPTOGRAPHY*” International Journal of Advances in Applied Science and Engineering (IJAEAS) e-ISSN : 2348-1811, ISSN(E) : 2348-182X Volume :01 Issue :04,september 2014.

## **Workshops Attended**

- Attended 2-day work shop on MSP 430 & TIVA MICROCONTROLLERS at JNTUA College of engineering, pulivendula, kadapa dist. in july 2016.