

Kandula Srinivasa Reddy Memorial College of Engineering (Autonomous)

Kadapa-516003. AP

(Approved by AICTE, Affiliated to JNTUA, Ananthapuramu, Accredited by NAAC)

(An ISO 9001-2008 Certified Institution)

Department of Electronics and Communication Engineering



Certification Course

On

“PCB Design”

Resource Person : Dr. P Lokeswara Reddy

Course Coordinator: Smt. T. Uma Maheswari

Duration : 18-04-2022 to 29-04-2022



K.S.R.M. COLLEGE OF ENGINEERING

(UGC - AUTONOMOUS)

Kadapa, Andhra Pradesh, India - 516003

Approved by AICTE, New Delhi & Affiliated to JNTUA, Ananthapuramu.

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Lr./KSRMCE/ (Department of ECE)/2020-21

Date: 11-04-2022

To
The Principal
KSRM College of Engineering
Kadapa, AP.

Sub: KSRMCE - (Department of ECE) – Permission to conduct a certification course on “PCB Design” – Request– reg.

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Respected Sir,

With reference to the cited, the Department of ECE is planning to conduct a certification course on “PCB Design” for All B.Tech IV SEM students from 18-04-2022 to 29-04-2022. In this regard, I kindly request you to grant us permission to conduct a certification course. This is submitted for your kind perusal.

Thanking you sir,

Yours Faithfully,

Coordinator

Smt.T. Uma Maheswari

Cc:

To The Director for Information

To All Deans/HODs

Permitted
V. S. S. Murthy
PRINCIPAL
K.S.R.M. COLLEGE OF ENGINEERING
KADAPA-516005, (A.P.)



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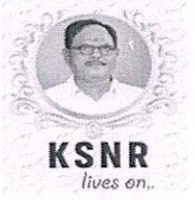
K.S.R.M. COLLEGE OF ENGINEERING

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Date: 12-04-2022

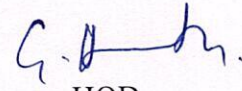
Circular

All the B.Tech III SEM students are hereby informed that the Department of ECE is going to conduct certification course on "PCB Design" from 18/04/2022 to 29/04/2022. Interested students may register their names with respective faculty members on or before 16/04/2022.

For any queries contact,

Coordinator

Smt.T. Uma Maheswari, Assistant Professor, ECE Dept.,


HOD

Professor & H.O.D.
Department of E.C.E.
K.S.R.M. College of Engineering
KADAPA - 516 003

Cc to:

The Management /Director / All Deans / All HODS/Staff / Students for information

The IQAC Cell for Documentation



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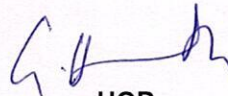
PCB Design Attendance Sheet

Date: 18/4/22

S.NO.	H.T.No.	NAME OF THE STUDENT	Signature	
			FN	AN
1	209Y1A0406	B MANASWITHA (W)	B. Manaswitha	B. Manaswitha
2	209Y1A0411	BANGASH SHAFIKHA (W)	B. Shafikha	B. Shafikha
3	209Y1A0416	BIJINEPALLI RAJYALAKSHMI (W)	R. Rajyalakshmi	R. Rajyalakshmi
4	209Y1A0417	BOGEM SANGEETHA (W)	R. Sangeetha	R. Sangeetha
5	209Y1A0421	ABDULLAH	C. S. Abdullah	C. S. Abdullah
6	209Y1A0426	CHAVALI ROHITHA YADAV (W)	C. Rohitha Yadav	C. Rohitha Yadav
7	209Y1A0428	CHEMIKELA KAVYANJALI (W)	C. Kavyanjali	C. Kavyanjali
8	209Y1A0433	CHINTHA HARIPRIYA (W)	C. Haripriya	C. Haripriya
9	209Y1A0436	DANDE BHARGAV	D. Bhargav	D. Bhargav
10	209Y1A0441	DEVAGANI SWETHA (W)	D. Swetha	D. Swetha
11	209Y1A0447	ELLURU NITHYA SREE (W)	E. Nithya sree	E. Nithya sree
12	209Y1A0454	GODLAVETI SAI PRATHAP	G. Sai Prathap	G. Sai Prathap
13	209Y1A0465	JETTI NAGAMANI (W)	J. Nagamani	J. Nagamani
14	209Y1A0471	KASETTI RAMYA SREE (W)	K. Ramya sree	K. Ramya sree
15	209Y1A0472	(W) KETHIREDDY BHAGYA LAKSHMI	K. Bhagya Lakshmi	K. Bhagya Lakshmi
16	209Y1A0473	KONERU DEEPA ASRITHA (W)	K. Deepa Asritha	K. Deepa Asritha
17	209Y1A0474	KOVVURU LAVANYA (W)	K. Lavanya	K. Lavanya
18	209Y1A0478	LAKSHMIGARI POOJITHA (W)	L. Poojitha	L. Poojitha
19	209Y1A0482	MADDEPALLI SUJITHA RANI (W)	M. Sujitha	M. Sujitha
20	209Y1A0486	(W) MANNARU VENKATA PREETHI	M. V. Preethi	M. V. Preethi
21	209Y1A0488	MAREPALLI SUSHMITHA (W)	M. Sushmitha	M. Sushmitha
22	209Y1A0491	MOOLE SAI SWARUPA (W)	M. Sai Swarupa	M. Sai Swarupa
23	209Y1A0495	MUTHUKURU BHAVITHA (W)	M. Bhavitha	M. Bhavitha
24	209Y1A0496	NAGAYAPALLE GEETHA (W)	N. Geetha	N. Geetha
25	209Y1A0499	NALLANI NAVYA (W)	N. Navya	N. Navya
26	209Y1A04A1	NARPALA VIJAYALAKSHMI (W)	N. Vijaya lakshmi	N. Vijaya lakshmi
27	209Y1A04A2	NEELAKANTHAM PRIYA (W)	N. Priya	N. Priya
28	209Y1A04A3	NEELOLLA HARATHI (W)	N. Harathi	N. Harathi
29	209Y1A04A6	PALLENNAGARI BHUMIKA (W)	P. Bhumika	P. Bhumika
30	209Y1A04A7	PANDILLAPALLE NANDINI (W)	P. Nandini	P. Nandini
31	209Y1A04B0	PATUKURI MOUNIKA (W)	P. Mounika	P. Mounika
32	209Y1A04B2	PENDEM USHA RANI (W)	Usha Rani	Usha Rani
33	209Y1A04B7	RAMIREDDY GARI BHARATHI (W)	R. Bharathi	R. Bharathi
34	209Y1A04B8	RANGANI JYOTHIPRIYA (W)	R. Jyothipriya	R. Jyothipriya
35	209Y1A04C1	SABBASANI PRIYA (W)	S. Priya	S. Priya
36	209Y1A04C2	SANA YASHWANTH	S. Yashwanth	S. Yashwanth
37	209Y1A04C5	SHAIK BEEBI AYESHA SIDDIKA (W)	S. Ayesha	S. Ayesha
38	209Y1A04C9	SHAIK MOHAMMED TAYYAB	S. Md Tayyab	S. Md Tayyab
39	209Y1A04D4	SINGANAMALA VENKATA SAI	S. Sai	S. Sai
40	209Y1A04D6	SUDHA SUMANTH REDDY	S. Sumanth Reddy	S. Sumanth Reddy
41	209Y1A04D9	FAREEDUDDIN	S. Md. Fareeduddin	S. Md. Fareeduddin
42	209Y1A04F0	VADUGURU ARUN KUMAR	V. Arun	V. Arun

43	209Y1A04F2	VANGITI VARSHINI (W)	V. Varshini	V. Varshini
44	209Y1A04F8	YARRAKALVA MAHESWAR REDDY	Y. Maheswar Reddy	Y. Maheswar Reddy
45	219Y5A0402	B. PALLAVI	B. pallavi	B. pallavi
46	219Y5A0403	B. PARIMALA	B. Parimala	B. Parimala
47	219Y5A0405	G. KAVYA	G. Kavya	G. Kavya
48	219Y5A0408	G. MAINA	G. Maina	G. maina
49	219Y5A0409	K. RAGHAVENDRA	K. Raghavendra	K. Raghavendra
50	219Y5A0411	M. MOUNIKA	M. Mounika	M. Mounika
51	219Y5A0415	SANE ARUN KUMAR	S. Arun Kumar	S. Arun Kumar


Coordinator


HOD

Professor & H.O.D.
Department of E.C.E.
K.S.R.M. College of Engineering
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UNIT-I

Introduction to PCB: Definition and Need of PCB, Background and History of PCB, Types of PCB, Classes of PCB Design, Terminology in PCB Design, Different Electronic design automation (EDA) tools and comparison.

UNIT-II

Introduction to PCB tools: Introduction of PCB, LIVE WIRE & PCB WIZARD software installation.

Live wire software: Explanation of each and every component of PCB and some basic circuits in Live wire software, Button interfacing with LED circuit, Power circuit, LDR interfacing with LED circuit, Potentiometer interfacing with LED circuit in Livewire software. Button interfacing with motor circuit, 555 timer using LED blinking, fire alarm circuit, police siren circuit, 4026 decade counter circuit in Livewire software.

UNIT-III

PCB WIZARD: Introduction of PCB WIZARD software, Explanation of each and every components and basic PCB circuits in PCB WIZARD software. Button interfacing with LED using PCB designing, LDR interfacing with LED using PCB designing, Potentiometer interfacing with LED using PCB designing. 555 timer using LED blinking designing(Astable and Bistable), fire alarm circuit designing, traffic lights circuit designing, explanation of manual routing and auto routing designing in PCB wizard software.

UNIT-IV

EASYEDA: Introduction of EASYEDA software, Explanation of each and every components and Explanation of basic PCB circuits LED, button, LDR, panic alarm, brightness control, Ac adapter, audio amplifier, common emitter IR sensor circuit, Astable and Bistable multivibrator using 555 timer circuit in EASYEDA software. Traffic lights circuit, motor circuit, explanation of 3D view, manual routing and auto routing designing in EASYEDA software.

UNIT-V

EAGLE: Introduction of EAGLE software, Explanation about circuit components, Basic Circuits explanation, explanation about auto routing and manual routing Astable and bistable circuit designing.

Text Books:

1. Walter Bosshart, "Printed Circuit Boards: Design and Technology", McGraw Hill Education.
2. Michael Dsouza, "PCB Design: Printed Circuit Board", Kindle Edition.



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Department of Electronics & Communication Engineering


Certificate Course on PCB Design (18/04/2022 - 29/04/2022)

Schedule

S.No	Date	Time	Faculty	Topic
1	18/04/2022	3 PM to 4 PM	Mr. ANUDEEP Mr. JAYA SHANKAR	Inauguration Introduction to PCB Concepts
2	18/04/2022	4 PM to 5 PM	Mr. JAYA SHANKAR Mr. ANUDEEP	Introduction to PCB Concepts
3	19/04/2022	3 PM to 4 PM	Mr. ANUDEEP Mr. JAYA SHANKAR	Introduction to PCB Fabrication
4	19/04/2022	4 PM to 5 PM	Mr. ANUDEEP Mr. JAYA SHANKAR	PCB Fabrication Methods
5	20/04/2022	3 PM to 4 PM	Mr. ANUDEEP Mr. JAYA SHANKAR	Different IC 555
6	20/04/2022	4 PM to 5 PM	Mr. ANUDEEP Mr. JAYA SHANKAR	Basic Circuits design using LIVE WIRE
7	21/04/2022	3 PM to 4 PM	Mr. ANUDEEP Mr. JAYA SHANKAR	Different IC 555 Basic Circuits design using LIVE WIRE
8	21/04/2022	4 PM to 5 PM	Mr. ANUDEEP Mr. JAYA SHANKAR	Different IC 555 Basic Circuits design using LIVE WIRE
9	22/04/2022	9 AM to 12 Noon	Mr. ANUDEEP Mr. JAYA SHANKAR	Different IC 555 Basic Circuits design using PCB Wizard
10	22/04/2022	1 PM to 4 PM	Mr. ANUDEEP Mr. JAYA SHANKAR	Different IC 555 Basic Circuits design using PCB Wizard
11	24/04/2022	9 AM to 12 Noon	Mr. ANUDEEP Mr. JAYA SHANKAR	Different IC 555 Basic Circuits design using PCB Wizard
12	25/04/2022	1 PM to 4 PM	Mr. ANUDEEP Mr. JAYA SHANKAR	Different IC 555 Basic Circuits design using Easy EDA
13	26/04/2022	9 AM to 12 Noon	Mr. ANUDEEP Mr. JAYA SHANKAR	Different IC 555 Basic Circuits design using Easy EDA
14	27/04/2022	1 PM to 4 PM	Mr. ANUDEEP Mr. JAYA SHANKAR	PCB Layout and etching process

15	28/04/2022	9 AM to 12 Noon	Mr. ANUDEEP Mr. JAYA SHANKAR	PCB Layout and etching process
16	29/04/2022	1 PM to 4 PM	Mr. ANUDEEP Mr. JAYA SHANKAR	PCB Layout and etching process Valedictory


Coordinator(s)


HOD

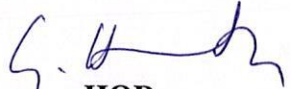
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10	209Y1A0441	DEVAGANI SWETHA (W)	P	P	P	P	A	P	P	A	P	P	P
11	209Y1A0447	ELLURU NITHYA SREE (W)	P	P	P	P	P	P	P	P	P	P	P
12	209Y1A0454	GODLAVETI SAI PRATHAP	P	P	P	P	P	P	A	P	P	P	P
13	209Y1A0465	JETTI NAGAMANI (W)	P	P	P	A	P	P	P	P	A	P	P
14	209Y1A0471	KASETTI RAMYA SREE (W)	P	P	P	P	A	A	P	P	P	P	P
15	209Y1A0472	KETHIREDDY BHAGYA LAKSHMI (W)	P	P	A	P	P	P	P	P	P	P	P
16	209Y1A0473	KONERU DEEPA ASRITHA (W)	P	P	P	P	P	A	P	A	P	A	P
17	209Y1A0474	KOVVURU LAVANYA (W)	P	A	P	P	P	P	P	P	P	P	P
18	209Y1A0478	LAKSHMIGARI POOJITHA (W)	P	P	P	P	P	P	P	P	A	P	P
19	209Y1A0482	MADDEPALLI SUJITHA RANI (W)	P	P	P	P	P	P	P	P	P	P	P
20	209Y1A0486	MANNURU VENKATA PREETHI (W)	P	P	P	P	P	P	P	P	P	P	P
21	209Y1A0488	MAREPALLI SUSHMITHA (W)	P	P	P	P	P	P	P	P	P	P	P
22	209Y1A0491	MOOLE SAI SWARUPA (W)	P	P	P	P	P	P	P	P	P	P	P
23	209Y1A0495	MUTHUKURU BHAVITHA (W)	P	P	P	P	P	P	P	P	P	P	P
24	209Y1A0496	NAGAYAPALLE GEETHA (W)	P	P	P	P	P	P	P	P	P	P	P
25	209Y1A0499	NALLANI NAVYA (W)	P	P	P	P	P	P	P	A	P	P	P
26	209Y1A04A1	NARPALA VIJAYALAKSHMI (W)	P	P	P	P	P	P	P	P	P	P	P
27	209Y1A04A2	NEELAKANTHAM PRIYA (W)	P	P	P	P	P	P	P	A	P	P	P

28	209Y1A04A3	NEELOLLA HARATHI (W)	P	P	P	P	P	P	A	P	P	P	P
29	209Y1A04A6	PALLENNAGARI BHUMIKA (W)	P	P	P	P	P	P	P	P	P	P	A
30	209Y1A04A7	PANDILLAPALLE NANDINI (W)	P	P	P	P	P	P	P	P	P	P	P
31	209Y1A04B0	PATUKURI MOUNIKA (W)	P	P	A	P	P	P	A	P	P	P	P
32	209Y1A04B2	PENDEM USHA RANI (W)	P	P	A	P	P	P	P	A	P	P	P
33	209Y1A04B7	RAMIREDDY GARI BHARATHI (W)	P	P	P	P	P	P	P	P	P	P	P
34	209Y1A04B8	RANGANI JYOTHIPRIYA (W)	P	P	P	P	A	P	P	P	P	P	P
35	209Y1A04C1	SABBASANI PRIYA (W)	P	P	P	P	A	P	P	P	P	P	P
36	209Y1A04C2	SANA YASHWANTH	P	P	P	P	P	P	P	P	P	P	P
37	209Y1A04C5	SHAIK BEEBI AYESHA SIDDIKA (W)	P	P	P	P	P	P	P	P	P	P	A
38	209Y1A04C9	SHAIK MOHAMMED TAYYAB	P	P	P	P	P	A	A	P	P	P	P
39	209Y1A04D4	SINGANAMALA VENKATA SAI	P	P	P	P	P	P	P	P	P	P	P
40	209Y1A04D6	SUDHA SUMANTH REDDY	P	P	P	P	P	P	P	P	P	P	P
41	209Y1A04D9	SYED MOHAMMED FAREEDUDDIN	P	P	P	P	P	P	P	P	P	P	P
42	209Y1A04F0	VADUGURU ARUN KUMAR	P	P	P	P	P	P	P	P	P	P	P
43	209Y1A04F2	VANGITI VARSHINI (W)	P	P	P	P	A	P	P	P	P	P	P
44	209Y1A04F8	YARRAKALVA MAHESWAR REDDY	P	P	P	P	A	P	P	P	P	P	P

45	219Y5A0402	B.Pallavi	P	P	P	P	P	P	P	P	A	P	P
46	219Y5A0403	B.Parimala	P	P	P	P	P	A	P	P	P	P	P
47	219Y5A0405	G.Kavya	P	P	P	P	P	P	P	P	P	P	P
48	219Y5A0408	G.maina	P	A	P	P	P	P	P	A	P	P	P
49	219Y5A0409	K.Raghavendra	P	P	P	A	P	P	P	P	P	P	P
50	219Y5A0411	M.Mounika	P	P	P	P	P	P	P	P	P	P	P
51	219Y5A0415	Sane Arun Kumar	P	P	P	P	P	P	P	P	P	P	P


Coordinator


HOD
Professor & H.O.D.
Department of E.C.E.
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Live on



Department of ECE

Certification Course on "PCB DESIGN" in Association with APSSDC

18-04-2022

from 09:00AM to 04:00 PM



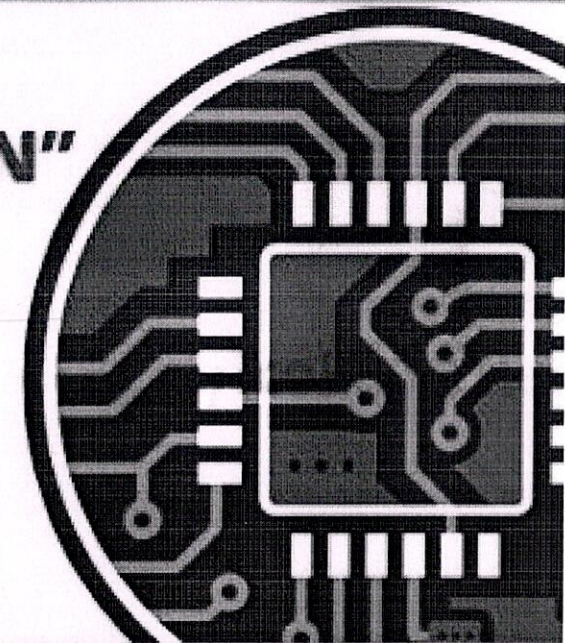
SJ IOT LAB-SJ215

Resource Persons

Sri Kuldeep & Sri Shankar
Technical Skill Trainers from APSSDC.

Coordinator

Smt.T. Uma Maheswari
Assistant Professor



Dr. G. Hemalatha	Dr. V.S.S. Murthy	Dr. Kandula Chandra Obul Reddy	Smt. K. Rajeswari	Sri K. Madan Mohan Reddy	Sri K. Raja Mohan Reddy
H.O.D	Principal	Managing Director	Correspondent Secretary, Treasurer	Vice-Chairman	Chairman

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ELECTRONICS & COMMUNICATION ENGINEERING

Activity Report

Name of the Activity	PCB DESIGN
Type of the Activity	Certification Course
Date of Activity	18-04-22 to 29-04-22
Details of Participants	ECE IV sem Students
Coordinator	T.Umamaheswari
Organizer/Supporting Team	Dept. of ECE
Report	<p>The Department of ECE in association with APSSDC organized a six day Certification Course from 18/04/22 to 29/04/22 . More than 50 participants successfully completed the course. On 18th ,Prof. G. Hemalatha, Head of the department, Electronics and Communication Engineering, addressed the gathering and welcomed all the participants. She shared the experience of conducting the events. Six hours per day, for 6 days, a total of 36 hours were successfully conducted with experienced subject's experts from APSSDC. The Valedictory function was conducted on 29/04/22 from 03.00PM-04.00PM. More than 50 participants successfully received participation certification. Many of the participants gave positive feedback and are able to design PCBs in software and Hardware.</p>



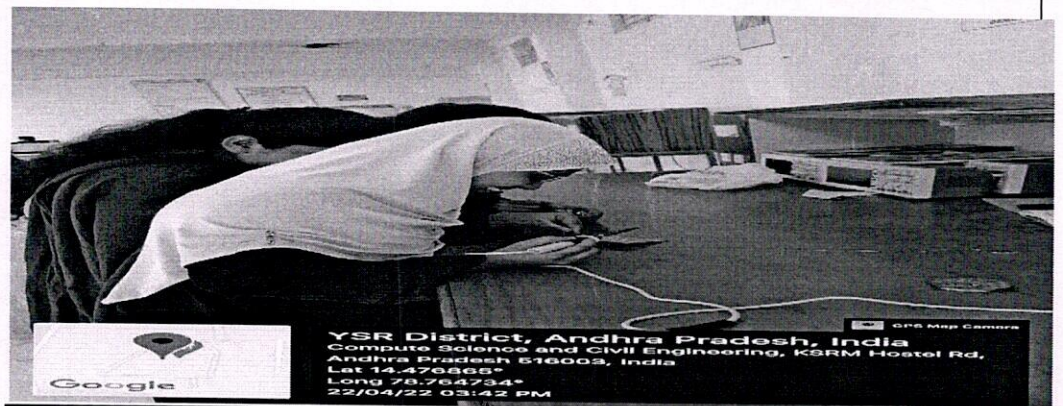
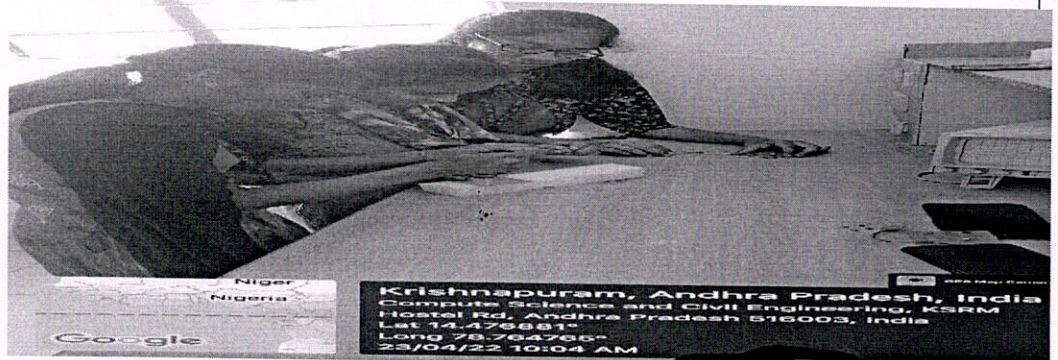
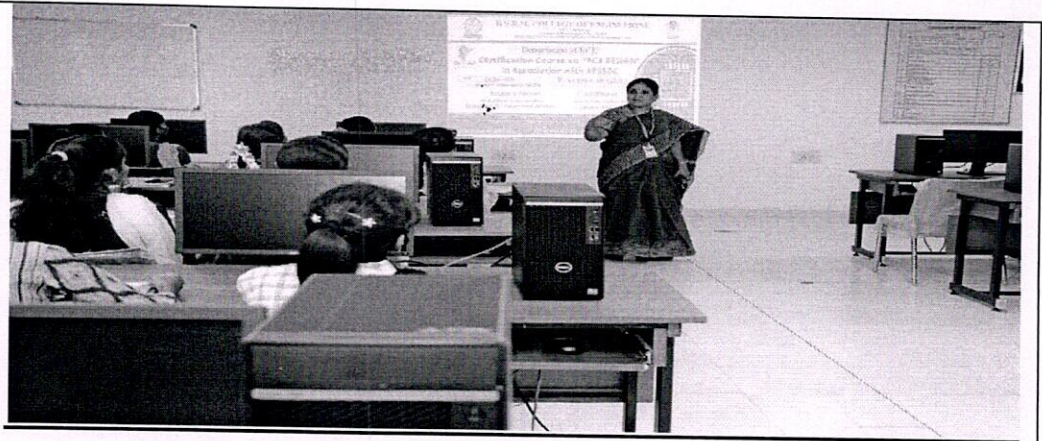
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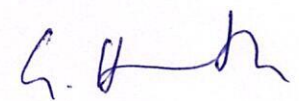


/ksrmceofficial

Photos




Coordinator


HOD
Professor & H.O.D.
Department of E.C.E.
K.S.R.M. College of Engineering
KADAPA - 516 093



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Certificate of Participation

This is to certify that

Mr/Ms SHAIK BEEBI AYESHA SIDDIKA with

Roll.No. 209Y1A04C5



has attended the Certification course on "PCB DESIGN" from
18th to 23rd April, 2022 organized by Dept. of Electronics and
Communication Engineering in Association with APSSDC

Coordinator

Smt T. Umamaheswari

Asst. Professor, ECE

Dr. G. Hemalatha

HOD, ECE

Prof V S S Murthy

Principal



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Certificate of Participation

Mr/Ms BOGGULA PARIMALA with Roll.No. 219Y5A0403
has attended the Certification course on " PCB Design" from
18-04-22 to 29-04-22 organized by Dept. of Electronics and
Communication Engineering.

Mrs.T.Umamaheswari
Co-coordinator

Dr. G. Hemalatha
HOD, ECE

Prof V S S Murthy
Principal



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Certificate of Participation

This is to certify that

Mr/Ms SANE ARUN KUMAR with Roll.No. 219Y5A0415
has attended the Certification course on " PCB Design" from
18-04-22 to 29-04-22 organized by Dept. of Electronics and
Communication Engineering.

Mrs.T.Umamaheswari
Co-coordinator

Dr. G. Hemalatha
HOD, ECE

Prof V S S Murthy
Principal

Feedback form on Certificate Course

PCB DESIGN(18/04/2022 to 29/04/2022)

* Required

1. Roll Number *

2. Name of the Student *

3. B.Tech Semester *

Mark only one oval.

☐ I SEM

☐ II SEM

☐ III SEM

☐ IV SEM

☐ V SEM

☐ VI SEM

☐ VII SEM

☐ VIII SEM

4. Branch *

Mark only one oval.

☐ Civil Enginerring

☐ EEE

☐ ME

☐ ECE

☐ CSE

☐ AI&ML

5. Email ID *

6. Is the course content meet your exception. *

Mark only one oval.

☐ Yes

☐ No

7. Is the lecture sequence well planned. *

Mark only one oval.

☐ Strongly disagree

☐ Disagree

☐ Neutral

☐ Agree

☐ Strongly agree

8. The contents of the course is explained with examples. *

Mark only one oval.

- ☐ Strongly disagree
☐ Disagree
☐ Neutral
☐ Agree
☐ Strongly Agree

9. Is the level of course high. *

Mark only one oval.

- ☐ Strongly disagree
☐ Disagree
☐ Neutral
☐ Agree
☐ Strongly Agree

10. Is the course exposed you to the new knowledge and practice. *

Mark only one oval.

- ☐ Strongly disagree
☐ Disagree
☐ Neutral
☐ Agree
☐ Strongly Agree

11. Is the lecture clear and easy to understand. *

Mark only one oval.

☐ Strongly disagree

☐ Disagree

☐ Neutral

☐ Agree

☐ Strongly agree

12. Rate the value of the course increasing your skills. *

Mark only one oval.

☐ Strongly disagree

☐ Disagree

☐ Neutral

☐ Agree

☐ Strongly Agree

13. Any suggestions

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Google Forms



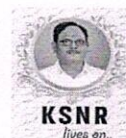
K.S.R.M. COLLEGE OF ENGINEERING

(UGC-AUTONOMOUS)

Kadapa, Andhra Pradesh, India- 516 003

Approved by AICTE, New Delhi & Affiliated to JNTUA, Ananthapuramu.

An ISO 14001:2004 & 9001: 2015 Certified Institution



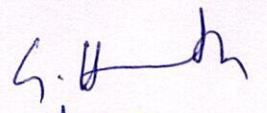
Department of Electronics & Communication Engineering

Feed back of certification Course on PCB Design

S.no,	Roll Number	Name of the Student	Organization of Course and session	Clarity in content delivery.	Content is relevant and useful	Adequate opportunity to	Overall rating
1	209Y1A0405	A.shashikala	Good	Very good	Good	Good	Good
2	209Y1A0411	B. Shafikha	Excellent	Excellent	Excellent	Excellent	Excellent
3	208Y1A0428	C.Kavyanjali	Excellent	Excellent	Excellent	Excellent	Excellent
4	209Y1A0433	C.Haripriya	Excellent	Very good	Excellent	Excellent	Excellent
5	209Y1A0447	E. Nithya Sree	Very good	Fair	Very good	Very good	Very good
6	209Y1A04D6	SUDHA SUMANTH RE	Good	Good	Good	Good	Good
7	219Y5A0415	SANE ARUN KUMAR	Excellent	Excellent	Excellent	Excellent	Excellent
8	219y5a0408	GORLA MAINA	Excellent	Excellent	Excellent	Excellent	Excellent
9	209Y1A04C9	Shaik Mohammed Tay	Very good	Excellent	Very good	Excellent	Excellent
10	219y5a0409	Karella raghavendra	Excellent	Very good	Very good	Excellent	Excellent
11	209Y1A04C2	Sana yashwanth	Very good	Very good	Very good	Very good	Very good
12	209y1a0446	ELADI STALIN CHAKR	Excellent	Poor	Poor	Poor	Poor
13	209Y1A0454	G.Sai Prathap	Excellent	Excellent	Excellent	Excellent	Excellent
14	209y1a04c1	S. Priya	Excellent	Very good	Very good	Very good	Excellent
15	209Y1A04C5	SHAIK. BEEBI AYESH	Excellent	Excellent	Excellent	Fair	Excellent
16	209Y1A04D9	Syed Mohammed Fare	Excellent	Excellent	Excellent	Excellent	Excellent
17	219Y5A0403	B. Parimala	Excellent	Excellent	Very good	Very good	Excellent
18	219y5a0402	B. Pallavi	Excellent	Very good	Excellent	Very good	Excellent
19	219Y5A0411	Meesala mounika	Very good	Very good	Good	Very good	Excellent
20	209y1a04f8	Yarrakalva Maheswarra	Excellent	Very good	Very good	Excellent	Excellent
21	209y1a0426	C. Rohithayadav	Good	Excellent	Very good	Very good	Excellent
22	209y1a04f0	Arun Kumar	Good	Good	Very good	Good	Very good
23	209Y1A0441	D.Swetha	Very good	Very good	Excellent	Good	Good
24	209Y1A0416	B.Rajya lakshmi	Excellent	Excellent	Excellent	Excellent	Excellent
25	209Y1A0406	B.Manaswitha	Very good	Very good	Very good	Very good	Very good
26	209y1a04d0	Shaik Waseet	Very good	Very good	Very good	Very good	Very good
27	209y1a0421	C.s.Abdullah	Very good	Excellent	Excellent	Excellent	Excellent
28	219Y5A0404	E.KIRAN KUMAR	Good	Good	Good	Good	Good

29	209Y1A0416	BIJINEPALLI RAJYAL	Very good	Fair	Very good	Very good	Very good
30	209Y1A0417	BOGEM SANGEETHA	Good	Good	Good	Good	Good
31	209Y1A0421	CHABUKSAWAR SHA	Excellent	Excellent	Excellent	Excellent	Excellent
32	209Y1A0426	CHAVALI ROHITHA Y	Excellent	Excellent	Excellent	Excellent	Excellent
33	209Y1A0428	CHEMIKELA KAVYAN	Very good	Excellent	Very good	Excellent	Excellent
34	209Y1A0433	CHINTHA HARIPRIYA	Excellent	Very good	Very good	Excellent	Excellent
35	209Y1A0436	DANDE BHARGAV	Very good	Very good	Very good	Very good	Very good
36	209Y1A0441	DEVAGANI SWETHA	Excellent	Poor	Poor	Poor	Poor
37	209Y1A0447	ELLURU NITHYA SRE	Excellent	Excellent	Excellent	Excellent	Excellent
38	209Y1A0454	GODLAVETI SAI PRA	Excellent	Very good	Very good	Very good	Excellent
39	209Y1A0465	JETTI NAGAMANI (W)	Very good	Excellent	Very good	Excellent	Excellent
40	209Y1A0471	KASETTI RAMYA SRE	Excellent	Very good	Very good	Excellent	Excellent
41	209Y1A0472	KETHIREDDY BHAGY	Very good	Very good	Very good	Very good	Very good
42	209Y1A0473	KONERU DEEPA ASR	Excellent	Poor	Poor	Poor	Poor
43	209Y1A0474	KOVVURU LAVANYA	Excellent	Excellent	Excellent	Excellent	Excellent
44	209Y1A0478	LAKSHMIGARI POOJI	Excellent	Very good	Very good	Excellent	Excellent
45	209Y1A0482	MADDEPALLI SUJITH	Very good	Very good	Very good	Very good	Very good
46	209Y1A0486	MANNURU VENKATA	Excellent	Poor	Poor	Poor	Poor
47	209Y1A0488	MAREPALLI SUSHMIT	Excellent	Excellent	Excellent	Excellent	Excellent




HOD

Professor & H.O.D.
Department of E.C.E.
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K.S.R.M. COLLEGE OF ENGINEERING (AUTONOMOUS), KADAPA-516003
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
VALUE ADDED/CERTIFICATE COURSE ON
PCB Design FROM 18-04-22 to 29-04-22


AWARD LIST

S.No	Roll Number	Name of the Student	Marks Obtained
1.	209Y1A0406	B MANASWITHA (W)	08
2.	209Y1A0411	BANGASH SHAFIKHA (W)	06
3.	209Y1A0416	BIJINEPALLI RAJYALAKSHMI (W)	08
4.	209Y1A0417	BOGEM SANGEETHA (W)	09
5.	209Y1A0421	CHABUKSAWAR SHAIK ABDULLAH	05
6.	209Y1A0426	CHAVALI ROHITHA YADAV (W)	05
7.	209Y1A0428	CHEMIKELA KAVYANJALI (W)	06
8.	209Y1A0433	CHINTHA HARIPRIYA (W)	07
9.	209Y1A0436	DANDE BHARGAV	08
10.	209Y1A0441	DEVAGANI SWETHA (W)	09
11.	209Y1A0447	ELLURU NITHYA SREE (W)	08
12.	209Y1A0454	GODLAVETI SAI PRATHAP	09
13.	209Y1A0465	JETTI NAGAMANI (W)	05
14.	209Y1A0471	KASETTI RAMYA SREE (W)	08
15.	209Y1A0472	KETHIREDDY BHAGYA LAKSHMI (W)	09
16.	209Y1A0473	KONERU DEEPA ASRITHA (W)	06
17.	209Y1A0474	KOVVURU LAVANYA (W)	07
18.	209Y1A0478	LAKSHMIGARI POOJITHA (W)	08

19.	209Y1A0482	MADDEPALLI SUJITHA RANI (W)	09
20.	209Y1A0486	MANNURU VENKATA PREETHI (W)	09
21.	209Y1A0488	MAREPALLI SUSHMITHA (W)	08
22.	209Y1A0491	MOOLE SAI SWARUPA (W)	08
23.	209Y1A0495	MUTHUKURU BHAVITHA (W)	08
24.	209Y1A0496	NAGAYAPALLE GEETHA (W)	05
25.	209Y1A0499	NALLANI NAVYA (W)	05
26.	209Y1A04A1	NARPALA VIJAYALAKSHMI (W)	07
27.	209Y1A04A2	NEELAKANTHAM PRIYA (W)	07
28.	209Y1A04A3	NEELOLLA HARATHI (W)	07
29.	209Y1A04A6	PALLENNAGARI BHUMIKA (W)	08
30.	209Y1A04A7	PANDILLAPALLE NANDINI (W)	08
31.	209Y1A04B0	PATUKURI MOUNIKA (W)	09
32.	209Y1A04B2	PENDEM USHA RANI (W)	09
33.	209Y1A04B7	RAMIREDDY GARI BHARATHI (W)	08
34.	209Y1A04B8	RANGANI JYOTHIPRIYA (W)	08
35.	209Y1A04C1	SABBASANI PRIYA (W)	05
36.	209Y1A04C2	SANA YASHWANTH	08
37.	209Y1A04C5	SHAIK BEEBI AYESHA SIDDIKA (W)	09
38.	209Y1A04C9	SHAIK MOHAMMED TAYYAB	09
39.	209Y1A04D4	SINGANAMALA VENKATA SAI	08
40.	209Y1A04D6	SUDHA SUMANTH REDDY	07
41.	209Y1A04D9	SYED MOHAMMED FAREEDUDDIN	07
42.	209Y1A04F0	VADUGURU ARUN KUMAR	07
43.	209Y1A04F2	VANGITI VARSHINI (W)	08

44	209Y1A04F8	YARRAKALVA MAHESWAR REDDY	08
45	219Y5A0402	B.Pallavi	08
46	219Y5A0403	B.Parimala	08
47	219Y5A0405	G.Kavya	09
48	219Y5A0408	G.maina	04
49	219Y5A0409	K.Raghavendra	07
50	219Y5A0411	M.Mounika	06
51	219Y5A0415	Sane Arun Kumar	03


Coordinator


HoD
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K.S.R.M. College of Engineering
KADAPA - 516 083

K.S.R.M. COLLEGE OF ENGINEERING (AUTONOMOUS), KADAPA-516003
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
VALUE ADDED /CERTIFICATE COURSE ON
PCB Design FROM 18-04-22 to 29-04-22

ASSESSMENT TEST

Roll Number: 209Y1A0406 Name of the Student: B. Manaswitha

Time: 15 Min

(Objective Questions)

Max.Marks: 10

Note: Answer the following Questions and each question carries **one** mark.

1. High current circuits are purposely located or placed near the edge of PCB in accordance to the supply lines for _____. [B]
a) Removal of heat b) Isolation of stray current
c) Reduction of path length d) All of the above
2. What is a firm structure with targets and planes embedded in it known as
a)Electrical Device b) Machine c) System d) PCB [A]
3. Where the components placed on the board are soldered? [B]
a)Traces b) Planes c) Metal Pads d) Regions
4. How many layers does a PCB have? [C]
a)Single b) Double c) Multiple d) All Mentioned Above
5. What are the general categories of PCBs? [A]
a)Rigid b) Flex c) Metal-core d) All Mentioned Above
6. FR-4 is commonly used in _____. [B]
a)Rigid b) Flex c) Metal-core d) All Mentioned Above
7. Flexible boards have _____ thickness compared to standard boards? [C]
a)Less b) More c) Severe d) Moderate
8. How a PCB board can be designed initially? [D]
a)Using a Hardware b) Using CAD Tools
c) Using Electronic Devices d)Using Equipment's
9. What is included in library CAD parts in PCB design? [A]
a) Schematic Symbols b) Simulation Models
c)PCB Layout Footprints d) All Mentioned Above
10. Validation in pre-layout of PCB design involves verification of [A]
a)Manufacturer Part Numbers b)Vendor Part Numbers
c)Virtual Private Network d) a & b

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VALUE ADDED /CERTIFICATE COURSE ON
PCB Design FROM 18-04-22 to 29-04-22

ASSESSMENT TEST

Roll Number: _____ **Name of the Student:** _____

Time: 15 Min

(Objective Questions)

Max.Marks: 10

Note: Answer the following Questions and each question carries **one** mark.

1. High current circuits are purposely located or placed near the edge of PCB in accordance to the supply lines for _____ []
a) Removal of heat b) Isolation of stray current
c) Reduction of path length d) All of the above
2. What is a firm structure with targets and planes embedded in it known as
a)Electrical Device b) Machine c) System d) PCB []
3. Where the components placed on the board are soldered? []
a)Traces b) Planes c) Metal Pads d) Regions
4. How many layers does a PCB have? []
a)Single b) Double c) Multiple d) All Mentioned Above
5. What are the general categories of PCBs? []
a)Rigid b) Flex c) Metal-core d) All Mentioned Above
6. FR-4 is commonly used in _____? []
a)Rigid b) Flex c) Metal-core d) All Mentioned Above
7. Flexible boards have _____ thickness compared to standard boards?[]
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8. How a PCB board can be designed initially? []
a)Using a Hardware b) Using CAD Tools
c) Using Electronic Devices d)Using Equipment's
9. What is included in library CAD parts in PCB design? []
a) Schematic Symbols b) Simulation Models
c)PCB Layout Footprints d) All Mentioned Above
10. Validation in pre-layout of PCB design involves verification of []
a)Manufacturer Part Numbers b)Vendor Part Numbers
c)Virtual Private Network d) a & b

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VALUE ADDED /CERTIFICATE COURSE ON
PCB Design FROM 18-04-22 to 29-04-22

09/10

ASSESSMENT TEST

Roll Number: 20941A0441 Name of the Student: D. SWATHA

Time: 15 Min

(Objective Questions)

Max.Marks: 10

Note: Answer the following Questions and each question carries **one** mark.

1. High current circuits are purposely located or placed near the edge of PCB in accordance to the supply lines for _____ [B]

a) Removal of heat b) Isolation of stray current
c) Reduction of path length d) All of the above

2. What is a firm structure with targets and planes embedded in it known as

a)Electrical Device b) Machine c) System d) PCB [A]

3. Where the components placed on the board are soldered? [D]

a)Traces b) Planes c) Metal Pads d) Regions

4. How many layers does a PCB have? [C]

a)Single b) Double c) Multiple d) All Mentioned Above

5. What are the general categories of PCBs? [A]

a)Rigid b) Flex c) Metal-core d) All Mentioned Above

6. FR-4 is commonly used in _____? [B]

a)Rigid b) Flex c) Metal-core d) All Mentioned Above

7. Flexible boards have _____ thickness compared to standard boards? [C]

a)Less b) More c) Severe d) Moderate

8. How a PCB board can be designed initially? [D]

a)Using a Hardware b) Using CAD Tools
c) Using Electronic Devices d)Using Equipment's

9. What is included in library CAD parts in PCB design? [A]

a) Schematic Symbols b) Simulation Models
c)PCB Layout Footprints d) All Mentioned Above

10. Validation in pre-layout of PCB design involves verification of [A]

a)Manufacturer Part Numbers b)Vendor Part Numbers
c)Virtual Private Network d) a & b

K.S.R.M. COLLEGE OF ENGINEERING (AUTONOMOUS), KADAPA-516003
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VALUE ADDED /CERTIFICATE COURSE ON
PCB Design FROM 18-04-22 to 29-04-22

ASSESSMENT TEST

Roll Number: 209Y1A04B2 Name of the Student: P. Usha

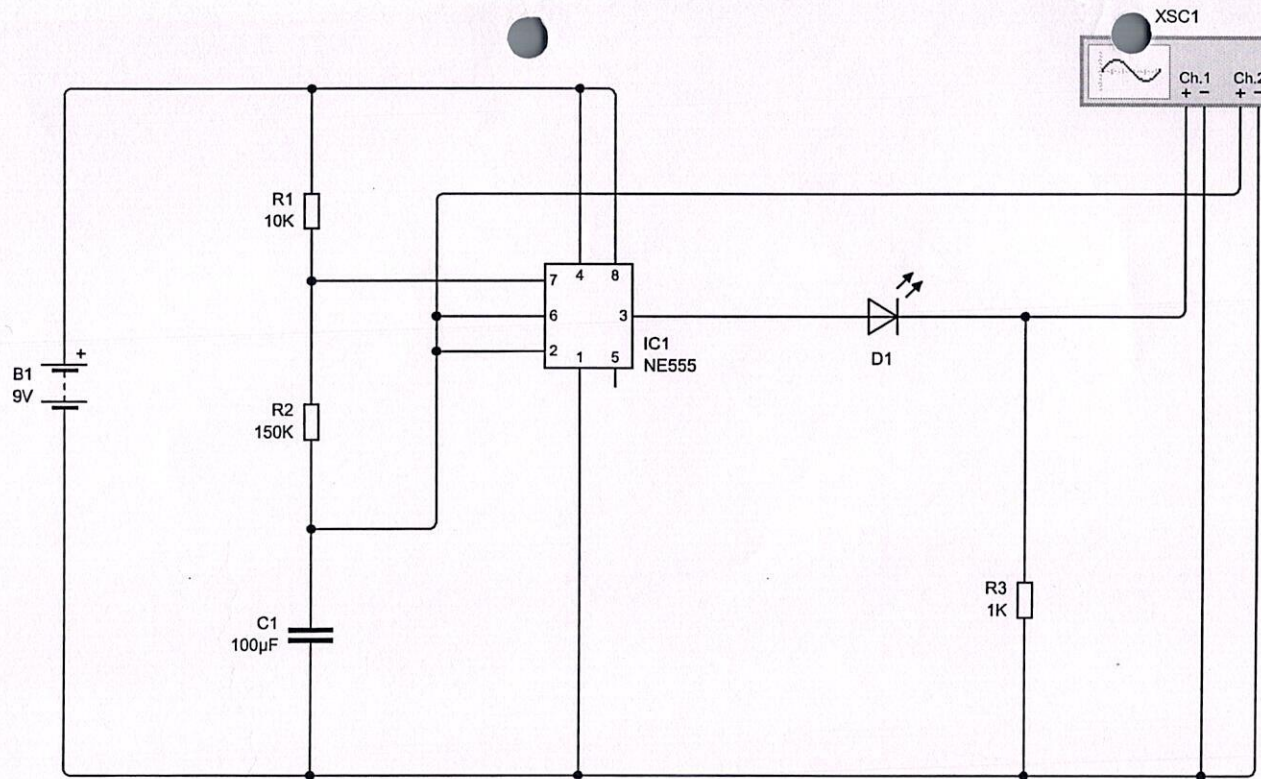
Time: 15 Min

(Objective Questions)

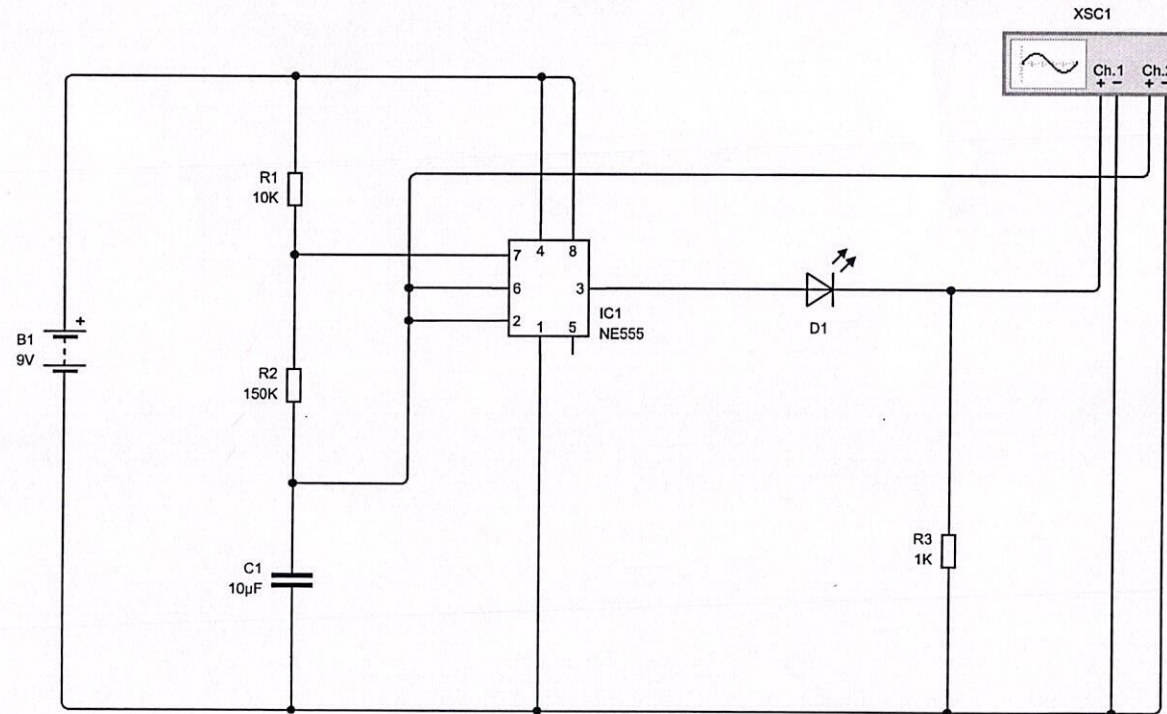
Max.Marks: 10

Note: Answer the following Questions and each question carries one mark.

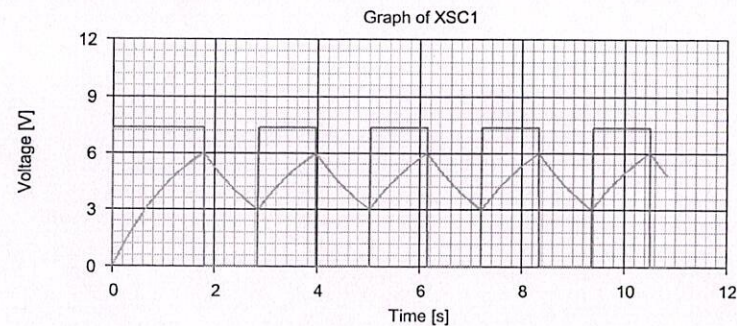
1. High current circuits are purposely located or placed near the edge of PCB in accordance to the supply lines for _____ [B]
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c) Reduction of path length d) All of the above
2. What is a firm structure with targets and planes embedded in it known as
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3. Where the components placed on the board are soldered? [B]
a) Traces b) Planes c) Metal Pads d) Regions
4. How many layers does a PCB have? [C]
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10. Validation in pre-layout of PCB design involves verification of [A]
a) Manufacturer Part Numbers b) Vendor Part Numbers
c) Virtual Private Network d) a & b



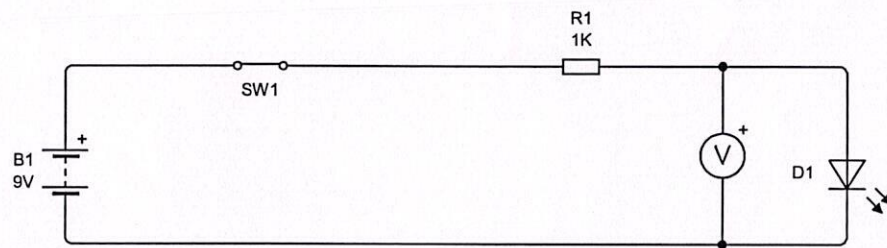
Astable Multivibrator Using 555 Timer



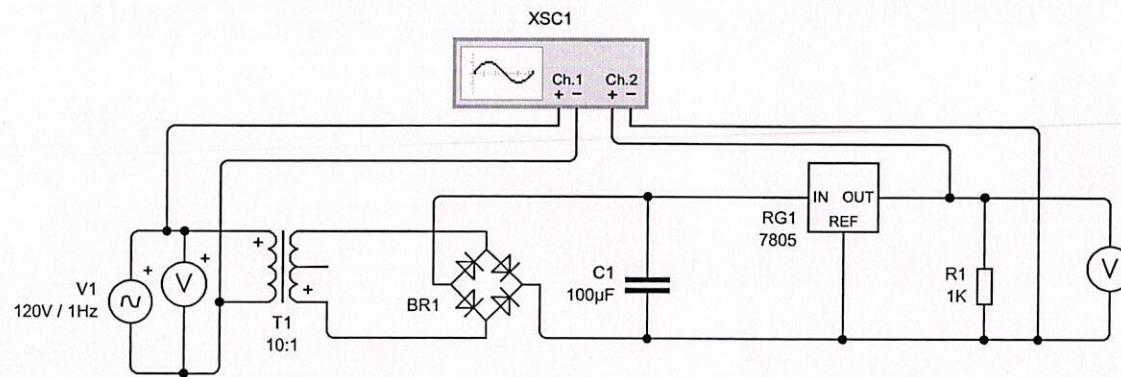
Output Waveforms



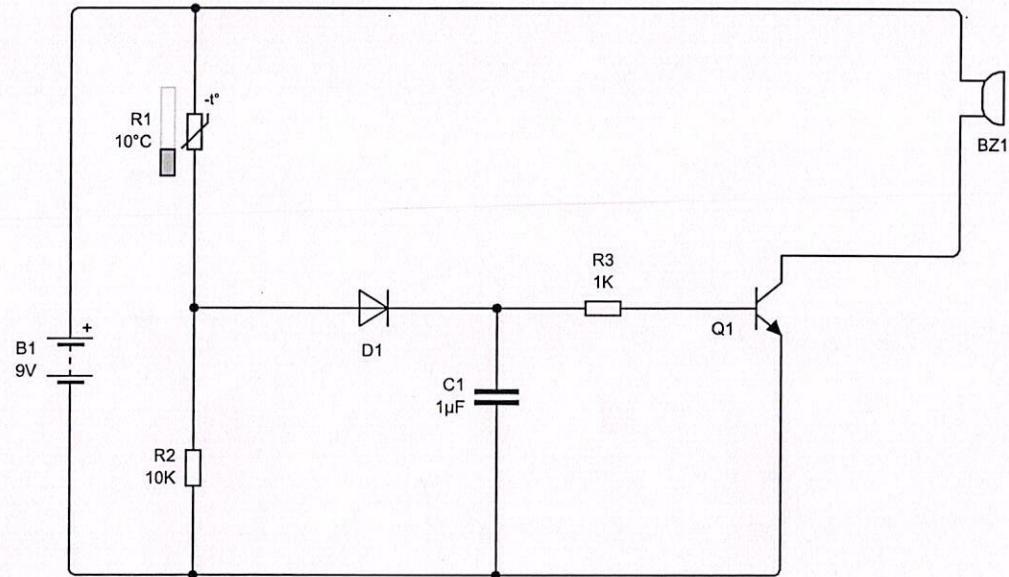
Button Interfacing With LED



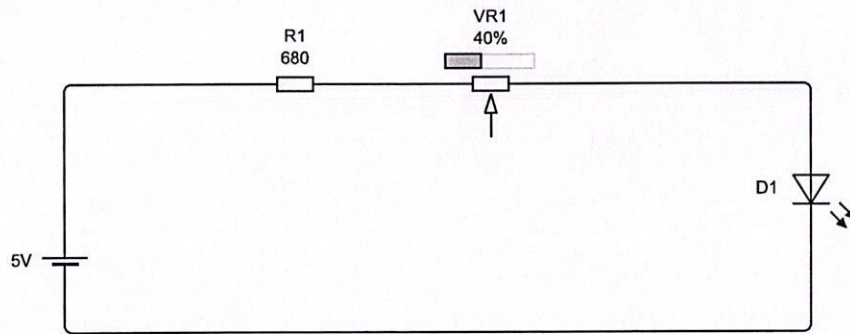
Design of Regulated Power Supply



Fire Alarm Circuit



Light Dependant Resistor LDR



Power Circuit

