

K.S.R.M.COLLEGE OF ENGINEERING (AUTONOMOUS)

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(Accredited by NAAC & NBA)

(An ISO 9001:2015 Certified Institution)

KADAPA – 516005 (A.P.)

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING



CERTIFICATE

This is to certify that the project report entitled “ADAPTIVE FILTERING TECHNIQUES FOR NOISE CANCELLATION”, is being submitted by G.HARSHITHA (209Y1A0459), C.V.NAGENDRA KUMAR REDDY (209Y1A0432), A.UDAY KUMAR REDDY (209Y1A0404), C.S.ABDULLAH (209Y1A0421), C.VENKATA LOHITH (209Y1A0425) to K.S.R.M. College of Engineering (AUTONOMOUS), Kadapa in partial fulfilment of the requirements for the award of the degree of “BACHELOR OF TECHNOLOGY” in “ELECTRONICS AND COMMUNICATION ENGINEERING” is a bonafide record of the project work carried out by them under our supervision during the period 2023-2024.

Project guide

Dr. G. HEMALATHA, M.Tech., Ph.D.

Professor & HOD,

Dept. of E.C.E.

Head of the Department

Dr. G. HEMALATHA, M.Tech., Ph.D.

Professor & HOD,

Dept. of E.C.E.

Date: 29/04/2024.

Internal Examiner

External Examiner

29/4/24

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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING



CERTIFICATE

This is to certify that the project report entitled **“DESIGN OF SPI MASTER CORE”**, is being submitted by **B. SANGEETHA (209Y1A0417), D. VENKAT RAMESH (209Y1A0438), D. VENUGOPAL (209Y1A0444), G. NITHIN KUMAR REDDY (209Y1A0450), G.NAGENDRA PRASAD (209Y1A0453)** to K.S.R.M. College of Engineering (AUTONOMOUS), Kadapa in partial fulfilment of the requirements for the award of the degree of **“BACHELOR OF TECHNOLOGY” in “ELECTRONICS AND COMMUNICATION ENGINEERING”** is a bonafide record of the project work carried out by them under our supervision during the period 2023- 2024.

Project guide 
Sri R.V. Sreehari, M.E.,


Associate Professor,
Dept. of E.C.E.


Head of the Department
Dr. G. HEMALATHA, M.Tech., Ph.D.

Professor & HOD,
Dept. of E.C.E.

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External Examiner
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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING



CERTIFICATE

This is to certify that the project report entitled "CNN BASED IMAGE RECONSTRUCTION METHOD FOR ULTRAFAST ULTRASOUND IMAGING", is being submitted by B. MANASWITHA (209Y1A0406), D. BHARGAV(209Y1A0436), G. RAGHU(209Y1A0448), B.NARESH(209Y1A0412), B.SUDHEER (209Y1A0419) to K.S.R.M. College of Engineering (AUTONOMOUS), Kadapa in partial fulfilment of the requirements for the award of the degree of "BACHELOR OF TECHNOLOGY" in "ELECTRONICS AND COMMUNICATION ENGINEERING" is a bonafide record of the project work carried out by them under our supervision during the period 2023-2024.


Project guide

Dr. K.Pavan Kumar, M.Tech., Ph.D.,
Assistant Professor,
Dept. of E.C.E.


Head of the Department

Dr. G. HEMALATHA, M.Tech., Ph.D.
Professor & HOD,
Dept. of E.C.E.

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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING



CERTIFICATE

This is to certify that the project report entitled "DIGITAL ALARM CLOCK USING MODEL SIM", is being submitted by D.SWETHA (209Y1A0441), B.ANUSHA(209Y1A0408), C.GANESH(209Y1A0435), D.SIVAIAH(209Y1A0437), D.GANESH (209Y1A0439) to K.S.R.M. College of Engineering (AUTONOMOUS), Kadapa in partial fulfilment of the requirements for the award of the degree of "BACHELOR OF TECHNOLOGY" in "ELECTRONICS AND COMMUNICATION ENGINEERING" is a bonafide record of the project work carried out by them under our supervision during the period 2023-2024.


29/4/24

Project guide


Sri.M.Prabhakar, M.Tech.,
Associate Professor,
Dept. of E.C.E.

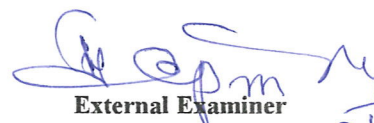


Head of the Department

Dr. G. HEMALATHA, M.Tech., Ph.D.
Professor & HOD,
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Date: 29/04/2024


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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING



CERTIFICATE

This is to certify that the project report entitled "DESIGN AND IMPLEMENTATION OF POSIT BASED ADDER AND MULTIPLIER USING VERILOG HDL", is being submitted by B. SHAFIKHA (209Y1A0411), C. VENKATA ANUHYA (209Y1A0423), G. VAMSI (209Y1A0449), B.BHARATH SIMHA REDDY (209Y1A0410), C. KARTHIK REDDY (209Y1A0429) to K.S.R.M. College of Engineering (AUTONOMOUS), Kadapa in partial fulfilment of the requirements for the award of the degree of "BACHELOR OF TECHNOLOGY" in "ELECTRONICS AND COMMUNICATION ENGINEERING" is a bonafide record of the project work carried out by them under our supervision during the period 2023-2024.



Project guide

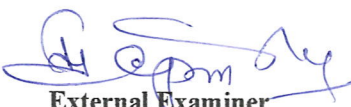
Smt. K.DIVYA LAKSHMI, M.Tech.,
Assistant Professor,
Dept. of E.C.E.


Head of the Department

Dr. G. HEMALATHA, M.Tech., Ph.D.
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Internal Examiner


External Examiner
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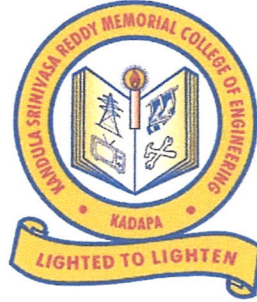
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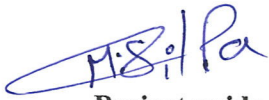
KADAPA – 516005 (A.P.)

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING



CERTIFICATE

This is to certify that the project report entitled "SMART TEMPERATURE DEPENDENT COOLING OF SOLAR PANEL USING ARDUINO", is being submitted by C.HARIPRIYA (209Y1A0433), D.JAYACHANDRA (209Y1A0445), D.ANANTHA REDDY(209Y1A0443),C.PRABHAKAR(209Y1A0422),E.STALIN CHAKRAVARTHY (209Y1A0446) to K.S.R.M. College of Engineering (AUTONOMOUS), Kadapa in partial fulfilment of the requirements for the award of the degree of "BACHELOR OF TECHNOLOGY" in "ELECTRONICS AND COMMUNICATION ENGINEERING" is a bonafide record of the project work carried out by them under our supervision during the period 2023-2024.



Project guide

Smt.M. Silpa, M.Tech.,
Assistant Professor,
Dept. of E.C.E.

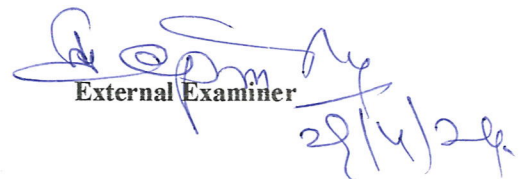


Head of the Department

Dr. G. HEMALATHA, M.Tech., Ph.D.
Professor & HOD,
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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING



CERTIFICATE

This is to certify that the project report entitled “**DESIGN OF LOWPASS FIR FILTER USING FREQUENCY SAMPLING METHOD**”, is being submitted by **C. MADHAVI (209Y1A0430), C. PRADEEP KUMAR REDDY (209Y1A0424), B. RAVIKANTH REDDY (209Y1A0414), A. SAIKRISHNA ACHARI (209Y1A0407), A. SAMSON (209Y1A0403)** to **K.S.R.M. College of Engineering (AUTONOMOUS), Kadapa** in partial fulfilment of the requirements for the award of the degree of “**BACHELOR OF TECHNOLOGY**” in “**ELECTRONICS AND COMMUNICATION ENGINEERING**” is a bonafide record of the project work carried out by them under our supervision during the period 2023-2024.


Project guide

Smt. S. Sharmila Banu, M.Tech.,
Assistant Professor,
Dept. of E.C.E.

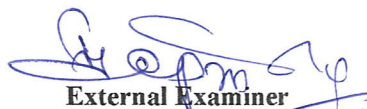

Head of the Department

Dr. G. HEMALATHA, M.Tech., Ph.D.
Professor & HOD,
Dept. of E.C.E.

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29/4/24


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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING



CERTIFICATE

This is to certify that the project report entitled "OPTIMIZATION OF WIFI COMMUNICATION SYSTEM USING LOW POWER RING OSCILLATOR DELAY CELL" is being submitted by C. KAVYANJALI (209Y1A0428), B. RAJYA LAKSHMI (209Y1A0416), A. SHASHI KALA (209Y1A0405), A. HARSHA VARDHAN (209Y1A0401), D. SAI KUMAR (199Y1A0433) to K.S.R.M. College of Engineering (AUTONOMOUS), Kadapa in partial fulfilment of the requirements for the award of the degree of "BACHELOR OF TECHNOLOGY" in "ELECTRONICS AND COMMUNICATION ENGINEERING" is a bonafide record of the project work carried out by them under our supervision during the period 2023-2024.

Project guide

Ms. G. Anitha Rani, M.Tech.,
Assistant Professor,
Dept. of E.C.E.

Head of the Department

Dr. G. HEMALATHA, M.Tech., Ph.D.
Professor & HOD,
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Date:

29/04/2024

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29/4/24

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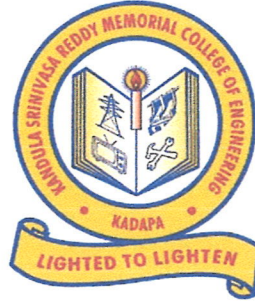
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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING



CERTIFICATE

This is to certify that the project report entitled "A NOVEL DESIGN OF HIGH SPEED & LOW POWER 4-BIT BINARY TO GRAY CODE CONVETER", is being submitted by D. MANOJ KUMAR (209Y1A0442), G. DADAVALI (209Y1A0455), C. RAJESHWAR REDDY (209Y1A0427), C. SIVA PAVANI (209Y1A0434), E. ASHIK (199Y1A0439) to K.S.R.M. College of Engineering (AUTONOMOUS), Kadapa in partial fulfilment of the requirements for the award of the degree of "BACHELOR OF TECHNOLOGY" in "ELECTRONICS AND COMMUNICATION ENGINEERING" is a bonafide record of the project work carried out by them under our supervision during the period 2023-2024.



Project guide

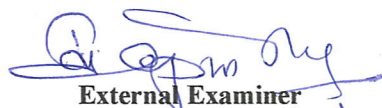
Dr. M. Madhusudhan Reddy, M.Tech., Ph.D.,
Associate Professor,
Dept. of E.C.E.


Head of the Department

Dr. G. HEMALATHA, M.Tech., Ph.D.,
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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING



CERTIFICATE

This is to certified that the project report entitled **"ONLINE SEMESTER FEE PAYING AND GENERATING NO DUE FORM"**, is being Submitted by **E. NITHYA SREE (209Y1A0447), B. KALYANI (209Y1A0418), C. RAJASEKHARNAIDU (209Y1A0431), G. TEJESWAR REDDY (209Y1A0452), G.SAI PRATHAP (209Y1A0454), D.V. BABRU VAHAN REDDY (209Y1A0440)** to **K.S.R.M. College of Engineering (AUTONOMOUS), Kadapa** in partial fulfilment of the requirements for the award of the degree of **"BACHELOR OF TECHNOLOGY"** in **"ELECTRONICS AND COMMUNICATION ENGINEERING"** is a bonafide record of the project work carried out by them under our supervision during the period **2022-2023**.


Project guide:

Dr. G. SUNEEL KUMAR, M. Tech, Ph.D.

Assistant Professor,

Dept. of E.C.E.



Head of the Department:


Dr. G. HEMALATHA, M.Tech., Ph.D.

Professor & HOD,

Dept. of E.C.E.

Date: 29/4/24


Internal Examiner


External Examiner
29/4/24

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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING



CERTIFICATE


This is to certified that the project report entitled “SMART HELMET FOR ACCIDENT DETECTION & ALERT SYSTEM BY USING STM32 MICROCONTROLLER” is being Submitted by, G.BHARGAVI(209Y1A0457), G.HARIVARDHAN(209Y1A0463),B.VIHARIKA(209Y1A0413),B.SWATHIKA(209Y1A0420),to K.S.R.M. College of Engineering (AUTONOMOUS), Kadapa in partial fulfillment of the requirements for the award of the degree of “BACHELOR OF TECHNOLOGY” in “ELECTRONICS AND COMMUNICATION ENGINEERING” is a bonafide record of the project work carried out by them under our supervision during the period 2023-2024.


Project guide:

Dr.S.L. PRATHAPA REDDY, M. Tech., Ph.D.,

Professor,

Dept. of E.C.E.


Head of the Department:

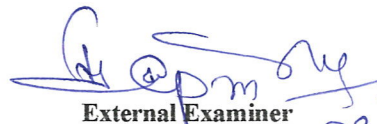
Dr. G. HEMALATHA, M.Tech., Ph.D.

Professor & HOD,

Dept. of E.C.E.

Date: 29/04/2024


Internal Examiner


External Examiner 29/4/24

7/12

K.S.R.M.COLLEGE OF ENGINEERING (AUTONOMOUS)

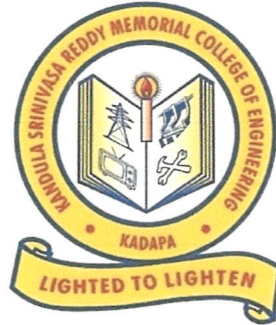
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KADAPA – 516005 (A.P.)

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING



CERTIFICATE

This is to certify that the project report entitled “A Novel Square Wave Generator Based on the Translinear Circuit Scheme of Second Generation Current Controlled Current Conveyor-CCCI”, is being submitted by G.SWETHA (209Y1A0451), B.KALYANI (209Y1A0409), C.ROHITHA YADAV (209Y1A0426), A.SIDDIQ (209Y1A0402), G.GURU PRASAD (209Y1A0458), to K.S.R.M. College of Engineering (AUTONOMOUS), Kadapa in partial fulfilment of the requirements for the award of the degree of “BACHELOR OF TECHNOLOGY” in “ELECTRONICS AND COMMUNICATION ENGINEERING “is a bonafide record of the project work carried out by them under our supervision during the period 2023-2024.

Project guide

Dr. Md. Mahaboob Pasha, M.Tech., Ph.D.,
Associate Professor,
Dept. of E.C.E.

Head of the Department

Dr. G. HEMALATHA, M.Tech., Ph.D.
Professor & HOD,
Dept. of E.C.E.

Date:

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KADAPA – 516005 (A.P.)

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING




CERTIFICATE

This is to certify that the project report entitled “A RIPPLE CARRY ADDER BASED ON HYBRID CMOS - MEMRISTOR LOGIC CIRCUIT”, is being submitted by M. SAI SWARUPA (209Y1A0491), P. MOUNIKA (209Y1A04B0), M. SUJITHA RANI (209Y1A0482), N. INDRA KALYAN REDDY (209Y1A0497), G. HARI BABU (209Y1A0462) to K.S.R.M. College of Engineering (AUTONOMOUS), Kadapa in partial fulfilment of the requirements for the award of the degree of “BACHELOR OF TECHNOLOGY” in “ELECTRONICS AND COMMUNICATION ENGINEERING” is a bonafide record of the project work carried out by them under our supervision during the period 2023-2024.

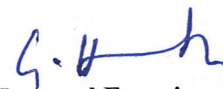

Project guide

Dr. V. ADINARAYANA REDDY, M.Tech., Ph.D.,
Professor
Dept. of E.C.E.


Head of the Department

Dr. G. HEMALATHA, M.Tech., Ph.D.,
Professor & HOD,
Dept. of E.C.E.

Date: 27/04/2024


Internal Examiner


External Examiner

B2

K.S.R.M. COLLEGE OF ENGINEERING (AUTONOMOUS)

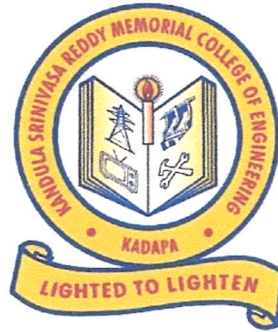
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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING



CERTIFICATE

This is to certify that the project report entitled “**DESIGN OF VGA CONTROLLER FOR THE APPLICATION OF SCREEN SAVER**”, is being submitted by **N.VIJAYA LAKSHMI (209Y1A04A1), K.LAVANYA (209Y1A0474), P.USHARANI(209Y1A04B2) , P.MANJUNATH(209Y1A04A9),M.AVINASHREDDY(209Y1A0489)** to **K.S.R.M. College of Engineering (AUTONOMOUS), Kadapa** in partial fulfilment of the requirements for the award of the degree of “**BACHELOR OF TECHNOLOGY**” in “**ELECTRONICS AND COMMUNICATION ENGINEERING**” is a bonafide record of the project work carried out by them under our supervision during the period 2023-2024.

Project guide

Dr.Md. MAHABOOB PASHA, M.Tech., Ph.D.
Associate Professor,
Dept. of E.C.E.

Head of the Department

Dr. G. HEMALATHA, M.Tech., Ph.D.
Professor & HOD,
Dept. of E.C.E.

Date: 27/04/2024.

Internal Examiner

External Examiner

B3

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KADAPA – 516005 (A.P.)

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING



CERTIFICATE

This is to certify that the project report entitled “Age and Gender Detection Using Machine Learning”, is being submitted by M.BHAVITHA (209Y1A0495) , J.NAGA MANI (209Y1A0465) , K.ANITHA (209Y1A0467) , R.JYOTHI PRIYA (159Y1A04B8) , P.VEERA DINESH (209Y1A04B5) to K.S.R.M. College of Engineering (AUTONOMOUS), Kadapa in partial fulfilment of the requirements for the award of the degree of “BACHELOR OF TECHNOLOGY” in “ELECTRONICS AND COMMUNICATION ENGINEERING” is a bonafide record of the project work carried out by them under our supervision during the period 2023-2024.



Project guide

Dr. G. Suneel Kumar, M.Tech., Ph.D.,
Assistant Professor,
Dept. of E.C.E.


Head of the Department

Dr. G. HEMALATHA, M.Tech., Ph.D.
Professor & HOD,
Dept. of E.C.E.

Date: 27/04/2024


Internal Examiner


External Examiner

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KADAPA – 516 005 (A.P.)

ELECTRONICS AND COMMUNICATIONS OF ENGINEERING



CERTIFICATE

This is to certify that the project report entitled "Personal Healthcare Chatbot for Medical Suggestions Using Artificial Intelligence and Machine Learning" is being submitted by CHANDRALEKHA PAIPADU (209Y1A04A5), NALLANI NAVYA (209Y1A0A99), NAGAYAPALLE GEETHA(209Y1A0496), MEKELA GIRINATH REDDY (209Y1A0490), LAGASANI VEERA CHARAN(209Y1A0475) to K.S.R.M. College of Engineering (AUTONOMOUS), Kadapa in partial fulfilment of the requirements for the award of the degree of "BACHELOR OF TECHNOLOGY" in "ELECTRONICS AND COMMUNICATIONS OF ENGINEERING" is a bonafide record of the project work carried out by them under our supervision during the period 2023-2024.

Project Guide

Sri. Y.VENKATESWARA RAJU M.Tech., (Ph.D.),

Assistant Professor, Dept. of E.C.E.

Head of the Department

Dr. G HEMELATHA, M.Tech., Ph.D.

Professor & HOD, Dept. of E.C.E

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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING



CERTIFICATE

This is to certify that the project report entitled **"IMPLEMENTATION OF HIGH SECURITY SOUND SYSTEM"**, is being submitted by P. BHUMIKA (209Y1A04A6), P. CHANDRA SEKHAR (209Y1A04A8), R. BHARATHI (209Y1A04B7), P. GIRINATH REDDY(209Y1A04B6), G. HARIKRISANA (209Y1A0461) to K.S.R.M. College of Engineering (AUTONOMOUS), Kadapa in partial fulfilment of the requirements for the award of the degree of **"BACHELOR OF TECHNOLOGY"** in **"ELECTRONICS AND COMMUNICATION ENGINEERING"** is a bonafide record of the project work carried out by them under our supervision during the period 2023-2024.


Project guide

Dr. M .Venkatanarayana, M.Tech., Ph.D.,
Professor and Dean, R&D Cell,
Dept. of E.C.E.


Head of the Department

Dr. G. Hemalatha, M.Tech., Ph.D.
Professor & HOD,
Dept. of E.C.E.

Date: 27/04/2024


Internal Examiner


External Examiner

36

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KADAPA – 516005 (A.P.)

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING



CERTIFICATE

This is to certified that the project report entitled “**DESIGN OF SERIAL PERIPHERAL INTERFACE MASTER CORE**”, is being submitted by **P. ROJA (209Y1A04B4), P. SUMANTH SREENIVAS (209Y1A04B3), N. SUBBARAYUDU (209Y1A04A0), M. RAJIV (209Y1A0493), K. ASHOK KUMAR (209Y1A0466)** to K.S.R.M. College of Engineering (AUTONOMOUS), Kadapa in partial fulfilment of the requirements for the award of the degree of “**BACHELOR OF TECHNOLOGY**” in **ELECTRONICS AND COMMUNICATION ENGINEERING**” is a bonafide record of the project work carried out by them under our supervision during the period 2023- 2024.

Project guide

Smt. Y. Sravanthi, M. Tech.,

Assistant Professor,

Dept. of E.C.E.

Head of the Department

Dr. G. HEMALATHA, M.Tech., Ph.D.

Professor & HOD,

Dept. of E.C.E.

Date: 27/04/2024

Internal Examiner

External Examiner

K.S.R.M.COLLEGE OF ENGINEERING (AUTONOMOUS)

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(An ISO 9001:2015 Certified Institution)

KADAPA – 516005 (A.P.)

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING



CERTIFICATE

This is to certify that the project report entitled “DESIGN OF SERIAL PERIPHERAL INTERFACE MASTER CORE”, is being submitted by M. SUSHMITHA(209Y1A0488), M. SARADA (209Y1A0492), M.V.PREETHI (209Y1A0486),M. GANESH (209Y1A0464), G.MANOJ(209Y1A0460) to K.S.R.M. College of Engineering (AUTONOMOUS),Kadapa in partial fulfilment of the requirements for the award of the degree of “BACHELOR OF TECHNOLOGY” in “ELECTRONICS AND COMMUNICATION ENGINEERING” is a bonafide record of the project work carried out by them under our supervision during the period 2023-2024.


Project guide

Smt. D. MAMATHA, M.Tech,

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Head of the Department

Dr.G. HEMALATHA, M.Tech.,Ph.D.,

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Dept. of E.C.E

Date: 27/04/2024


Internal Examiner


External Examiner

BS

K.S.R.M. COLLEGE OF ENGINEERING (AUTONOMOUS)

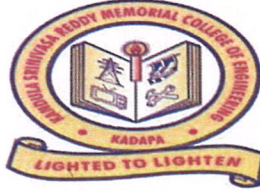
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(An ISO 9001:2015 Certified Institution)

KADAPA – 516005 (A.P.)

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING



CERTIFICATE

This is to certify that the project report entitled “Synthesis and Simulation of Digital Alarm clock by Using Verilog HDL”, is being submitted by K. Raghavendra Reddy (209Y1A0469), N. Priya (209Y1A04A2), N. Vasanth Kumar Reddy (209Y1A0498), K. Ganesh (209Y1A0470), M. Ravi Kumar (209Y1A0487), M. Vishnu Vardhan Reddy (209Y1A0484), to K.S.R.M. College of Engineering (AUTONOMOUS), Kadapa in partial fulfilment of the requirements for the award of the degree of “BACHELOR OF TECHNOLOGY” in “ELECTRONICS AND COMMUNICATION ENGINEERING” is a bonafide record of the project work carried out by them under our supervision during the period 2023-2024.

PA
27/4/24
Project guide

Sri. R.P.V.G Ashok Reddy, M.Tech., (Ph.D.),
Assistant Professor,
Dept. of E.C.E.

G. H. m
Head of the Department

Dr. G. HEMALATHA, M.Tech., Ph.D
Professor & HOD,
Dept. of E.C.E.

Date: 27/04/24

G. H. m
Internal Examiner

PA
External Examiner

69

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KADAPA – 516005 (A.P.)

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING



CERTIFICATE

This is to certify that the project report entitled “DESIGN OF A NOVEL SCHMITT TRIGGER USING SINGLE CURRENT AMPLIFIER”, is being submitted by K.RAMYA SREE (209Y1A0471), K.BHAGYA LAKSHMI (209Y1A0472), K.DEEPA ASRITHA (209Y1A0473), L.MANOJ(209Y1A0476) , M.RAMESH(209Y1A0483) to K.S.R.M. College of Engineering (AUTONOMOUS), Kadapa in partial fulfilment of the requirements for the award of the degree of “BACHELOR OF TECHNOLOGY” in “ELECTRONICS AND COMMUNICATION ENGINEERING” is a bonafide record of the project work carried out by them under our supervision during the period 2023-2024.

Project guide

Dr. SYED ZAHIRUDDIN M.Tech., Ph.D.,
Associate Professor,
Dept. of E.C.E.

Head of the Department

Dr. G. HEMALATHA, M.Tech., Ph.D.
Professor & HOD,
Dept. of E.C.E.

Date:

27/04/2024

Internal Examiner
External Examiner

B/C

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KADAPA – 516005 (A.P.)

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING



CERTIFICATE

This is to certify that the project report entitled "HIGH CMRR VOLTAGE MODE INSTRUMENTATION AMPLIFIER USING A NEW CMOS SECONDDGENERATION CURRENT CONTROLLED CONVEYOR REALIZATION", is being submitted by M. HARITHA (209Y1A0479), M.MUNI MAHESH (209Y1A0480), M.BRAMHANANDAM (209Y1A0481), M.KRISHNUSAIGOUD(209Y1A0485), P. NANADINI (209Y1A04A7) to K.S.R.M. College of Engineering (AUTONOMOUS), Kadapa in partial fulfillment of the requirements for the award of the degree of "BACHELOR OF TECHNOLOGY" in "ELECTRONICS AND COMMUNICATION ENGINEERING" is a bonafide record of the project work carried out by them under our supervision during the period 2023-2024.

Project guide

Dr. S. ZAHIRHUDDIN, M.Tech., Ph.D.,
Associate Professor,
Dept. of E.C.E.

Head of the Department

Dr. G. HEMALATHA, M.Tech., Ph.D.
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Date:

27/04/2024

Internal Examiner

External Examiner

B11

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(An ISO 9001:2015 Certified Institution)

KADAPA – 516005 (A.P.)

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING



CERTIFICATE

This is to certify that the project report entitled “DESIGNING OF VIDEO GRAPHICS ADAPTER FOR SCREEN SAVER APPLICATION”, is being submitted by L.V. SAIRAM (209Y1A0477), O.PRAVEEN KUMAR (209Y1A04A4), N. HARATHI (209Y1A04A3), L. POOJITHA (209Y1A0478), P.RAVITEJA REDDY (209Y1A04B1), K.SUNIL ACHARI (209Y1A0468) to K.S.R.M. College of Engineering (AUTONOMOUS), Kadapa in partial fulfilment of the requirements for the award of the degree of “BACHELOR OF TECHNOLOGY” in “ELECTRONICS AND COMMUNICATION ENGINEERING” is a bonafide record of the project work carried out by them under our supervision during the period 2023-2024.

Project guide

Sri P. SUBBARAYUDU, M.Tech,

Assistant Professor,

Dept. of E.C.E.

Head of the Department

Dr. G. HEMALATHA, M.Tech., Ph.D.

Professor & HOD,

Dept. of E.C.E.

Date: 24/04/2024

Internal Examiner

External Examiner

K.S.R.M.COLLEGE OF ENGINEERING (AUTONOMOUS)

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KADAPA – 516005 (A.P.)

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING



CERTIFICATE

This is to certify that the project report entitled “**ARDUINO BASED VEHICLE OVERLOAD SYSTEM FOR PREVENTION OF ACCIDENTS**”, is being submitted by **S.MD.FAREEDUDDIN(209Y1A04D9)**, **T.KUSUMA(209Y1A04E7)**, **S.CHARAN KUMAR (219Y5A0416)**, **Y.BHARATH(209Y1A04F7)**, **S.SANIYA (209Y1A04D2)** to **K.S.R.M. College of Engineering (AUTONOMOUS), Kadapa** in partial fulfilment of the requirements for the award of the degree of “**BACHELOR OF TECHNOLOGY**” in “**ELECTRONICS AND COMMUNICATION ENGINEERING**” is a bonafide record of the project work carried out by them under our supervision during the period 2023-2024.

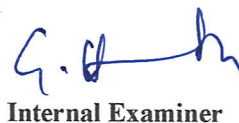

Project guide

Dr. S. L. PRATHAPA REDDY, M.Tech, Ph.D,
Professor,
Dept. of E.C.E.


Head of the Department

Dr. G. HEMALATHA, M.Tech., Ph.D.
Professor & HOD,
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Date: 26/04/24


Internal Examiner


External Examiner

C2

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KADAPA – 516005 (A.P.)

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING



CERTIFICATE

This is to certify that the project report entitled “PERFORMANCE ANALYSIS OF HIGH-SPEED AND LOW POWER MULTIPLIER USING REVERSIBLE GATES”, is being submitted by T. NITISH KUMAR REDDY (209Y1A04E3), V.SNEHALATHA REDDY (209Y1A04F6), S. MOHAMMED FAIZAN (209Y1A04C8), T. TARUN (209Y1A04E6), G. MADHU KUMAR (219Y5A0406) to K.S.R.M. College of Engineering (AUTONOMOUS), Kadapa in partial fulfilment of the requirements for the award of the degree of “BACHELOR OF TECHNOLOGY” in “ELECTRONICS AND COMMUNICATION ENGINEERING” is a bonafide record of the project work carried out by them under our supervision during the period 2023-2024.

M. Madhusudhan Reddy 26/4/24
Project guide

Dr. M. Madhusudhan Reddy, Ph.D.,
Associate Professor,
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G. Hemalatha
Head of the Department

Dr. G. HEMALATHA, M. Tech., Ph.D.,
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Date: 26/04/2024

G. Hemalatha
Internal Examiner

K. C. Kullappa
External Examiner

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KADAPA – 516005 (A.P.)

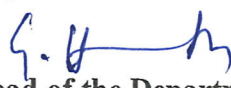
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING




CERTIFICATE

This is to certify that the project report entitled “DIGITAL SOUND_ALARM CLOCK”, is being submitted by SHAIK BEEBI AYESHA SIDDIKA (209Y1A04C5), PASUPULETI NAGENDRA (219Y5A0413), THIMMANAPALLE VAMSHI (209Y1A04E5), YELAMPALLE SAIKIRAN REDDY (209Y1A04F9), SHAIK MAHAMMAD (209Y1A04C6) to K.S.R.M. College of Engineering (AUTONOMOUS), Kadapa in partial fulfilment of the requirements for the award of the degree of “BACHELOR OF TECHNOLOGY” in “ELECTRONICS AND COMMUNICATION ENGINEERING” is a bonafide record of the project work carried out by them under our supervision during the period 2023-2024.


Project guide
Smt.S.SHARMILA BANU, M.Tech.
Assistant Professor,
Dept. of E.C.E.


Head of the Department
Dr. G. HEMALATHA, M.Tech., Ph.D.
Professor & HOD,
Dept. of E.C.E.


Internal Examiner


External Examiner

Date: 26/04/2024

CL

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KADAPA – 516005 (A.P.)


DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING



CERTIFICATE

This is to certify that the project report entitled **"IOT BASED HEALTH MONITORING SYSTEM"**, is being submitted by **S. FATHIMA MUBEENA (209Y1A04D7), V. NANDINI (209Y1A04F1), G.KAVYA (219Y5A0405), S. ARSHIYA (209Y1A04C0), V. SUDHEENDRA SAI (209Y1A04E9)** to **K.S.R.M. College of Engineering (AUTONOMOUS), Kadapa** in partial fulfilment of the requirements for the award of the degree of **"BACHELOR OF TECHNOLOGY"** in **"ELECTRONICS AND COMMUNICATION ENGINEERING"** is a bonafide record of the project work carried out by them under our supervision during the period 2023-2024.

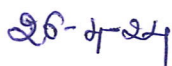
Project guide


Ms. S. Jabeen, M.Tech., (Ph.D.),
Assistant Professor,
Dept. of E.C.E.


Head of the Department

Dr. G. HEMALATHA, M.Tech., Ph.D.
Professor & HOD,
Dept. of E.C.E.

Date:


26-7-24


Internal Examiner


External Examiner

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KADAPA – 516005 (A.P.)

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING



CERTIFICATE

This is to certified that the project report entitled “CRAFTING A LOW-POWER, HIGH-PRECISION APPROXIMATE MULTIPLIER WITH ADAPTABLE TRUNCATION CAPABILITIES” is being submitted by SHAIK MAHAMMAD SHARIF (219Y5A0417), SABBASANI PRIYA (209Y1A04C1), SEETHI BABY SREE (209Y1A04C4), SANKAVARAM SREENIVASULU (209Y1A04C3), REDDEM SUMANTH REDDY (209Y1A04B9) to K.S.R.M. College of Engineering (AUTONOMOUS), Kadapa in partial fulfilment of the requirements for the award of the degree of “BACHELOR OF TECHNOLOGY” in “ELECTRONICS AND COMMUNICATION ENGINEERING” is a bonafide record of the project work carried out by them under our supervision during the period 2023-2024.



Project guide

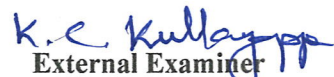
Sri G.A. SANJEEVA REDDY, M.Tech.,
Assistant Professor,
Dept. of E.C.E.


Head of the Department

Dr. G. HEMALATHA, M.Tech., Ph.D.,
Professor & HOD,
Dept. of E.C.E.

Date: 26/4/2024


Internal Examiner


External Examiner

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(An ISO 9001:2015 Certified Institution)


KADAPA – 516005 (A.P.)

DEPARTMENT OF ELECTRONICS & COMMUNICATION
ENGINEERING



CERTIFICATE

This is to certify that the project report entitled "DESIGN OF VIDEO GRAPHICS ADAPTER CONTROLLER FOR APPLICATION OF SCREEN SAVER", is being submitted by T.NARSIREDDY(209Y1A04E4),S.SUMANTH REDDY(209Y1A04D6),G.SARITHA(219Y5A0407),SMD.WASEET(209Y1A04 D0)M.SHIVA(219Y5A0412) to the K.S.R.M. College of Engineering (AUTONOMOUS), Kadapa in partial fulfilment of the requirements for the award of the degree of "BACHELOR OF TECHNOLOGY" in "ELECTRONICS AND COMMUNICATION ENGINEERING" is a bonafide record of the project work carried out by them under our supervision during the period 2023-2024.

Project guide: 
Smt.T.UMAMAHESWARI, M.Tech.(Ph.D.,)
Assistant Professor,
Dept. of E.C.E.


Head of the Department:
Dr.G.HEMALATHA, M.Tech.(Ph.D.)
Professor & HOD,
Dept. of E.C.E.

Date: 26/4/24  Internal Examiner


External Examiner

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(Approved by AICTE, New Delhi & Affiliated to JNTUA, Ananthapuramu)
KADAPA – 516 005 (A.P.)

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING



CERTIFICATE

This is to certify that the project report entitled “*DESIGN AND DEVELOPMENT OF MULTI-LEVEL INVERTER FOR SOLAR PHOTOVOLTAIC SYSTEM*” is being submitted by VANGITI VARSHINI (209Y1A04F2), SANE ARUN KUMAR (219Y5A0415), ENUPOTHULA KIRAN KUMAR (219Y5A0404), VANDADI BABY (219Y5A0418), SHAIK SAHEEL (209Y1A04D1) to K.S.R.M. College of Engineering (AUTONOMOUS), Kadapa in partial fulfillment of the requirements for the award of the degree of “*BACHELOR OF TECHNOLOGY*” in “*ELECTRONICS AND COMMUNICATION ENGINEERING*” is a bonafide record of the project work carried out by them under our supervision during the period 2023-2024.

Project Guide

Dr. SONU KUMAR MTech., Ph.D.
Associate Professor,
Dept of E.C.E.

Head of the Department

Dr. G. HEMALATHA, M.Tech., Ph.D.,
Professor & HOD,
Dept of E.C.E.

Date: 26/04/2024.

Internal Examiner

External Examiner

K.S.R.M. COLLEGE OF ENGINEERING (AUTONOMOUS)

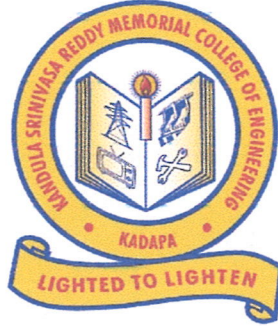
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KADAPA – 516005 (A.P.)

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING



CERTIFICATE

This is to certify that the project report entitled **“LOW -LIGHT IMAGE ENHANCEMENT USING A SIMPLE NETWORK STRUCTURE”**, is being submitted by **SHAIK MAHAMMAD RAFI (209Y1A04C7), BODDU PALLAVI (219Y5A0402), SYED UMAR MUKTHIYAR (209Y1A04E0), SHAIK ZUBARIYA (209Y1A04D3), MEESALA MOUNIKA (219Y5A0411)**, to **K.S.R.M. College of Engineering (AUTONOMOUS), Kadapa** in partial fulfilment of the requirements for the award of the degree of **“BACHELOR OF TECHNOLOGY”** in **“ELECTRONICS AND COMMUNICATION ENGINEERING”** is a bonafide record of the project work carried out by them under our supervision during the period 2023-2024.

Project guide 

Dr. K. Pavan Kumar, M.Tech., Ph.D.,
Assistant Professor,
Dept. of E.C.E.

Head of the Department 

Dr. G. HEMALATHA, M.Tech., Ph.D.
Professor & HOD,
Dept. of E.C.E.

Date:  26/04/2024


Internal Examiner


External Examiner

K.S.R.M COLLEGE OF ENGINEERING
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KADAPA – 516005 (A.P.)

**DEPARTMENT OF ELECTRONICS AND COMMUNICATION
ENGINEERING**



CERTIFICATE

This is to certified that the project report entitled “**PHYSICAL DESIGN IMPLEMENTATION OF SPI USING Q-FLOW**” is being submitted by **VEERAPURAM SWAPNA(209Y1A04F5)**, **SIRIGIREDDY SIVA RUCHITHA(209Y1A04D5)**, **GORLA MAINA(219Y5A0408)**, **LOKESH YADAV CHELLABOINA(219Y5A0410)**, **SYED MOHAMMED ASAD(209Y1A04D8)** to K.S.R.M. College of Engineering (AUTONOMOUS), Kadapa in partial fulfillment of the requirements for the award of the degree of “**BACHELOR OF TECHNOLOGY**” in “**ELECTRONICS AND COMMUNICATION ENGINEERING**” is a bonafide record of the project work carried out by them under our supervision during the period 2023-2024.

Project Guide

Dr.G.HEMALATHA,M.Tech.,Ph.D.,

Professor & HOD,

Dept. of E.C.E.

Head of the Department

Dr.G.HEMALATHA, M. Tech.,Ph.D.,

Professor & HOD,

Dept. of E.C.E.

Date: 26/04/2024

Internal Examiner
External Examiner

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KADAPA – 516005 (A.P.)


DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING



CERTIFICATE

This is to certify that the project report entitled **"FIRE FIGHTING ROBOT"**, is being submitted by **B. PARIMALA (219Y5A0403), T. VENKATA SAI (209Y1A04E2), T. REDDI KALA (209Y1A04E1), V. HARSHAVARDHAN (209Y1A04E8), V. ARUN KUMAR (209Y1A04F0)** to **K.S.R.M. College of Engineering (AUTONOMOUS), Kadapa** in partial fulfilment of the requirements for the award of the degree of **"BACHELOR OF TECHNOLOGY"** in **"ELECTRONICS AND COMMUNICATION ENGINEERING"** is a bonafide record of the project work carried out by them under our supervision during the period 2023-2024.


Project guide


Sri R.V. Sreenari, M.E.,
Associate Professor,
Dept. of E.C.E.


Head of the Department

Dr. G. HEMALATHA, M. Tech., Ph.D.
Professor & HOD,
Dept. of E.C.E.

Date: 26/4/24


Internal Examiner


External Examiner

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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING



CERTIFICATE

This is to certify that the project report entitled “ONE-SIDED SCHMITT-TRIGGER-BASED 9T SRAM CELL FOR NEAR-THRESH OLD OPERATION IN VLSI”, is being submitted by K. RAGHAVENDRA (219Y5A0409), V. PALLAVI (209Y1A04F3), P. FARUQ KHAN (219Y5A0414), B. JAGADEESHWAR REDDY (219Y5A0401), V. MADHU SAI VENKAT (209Y1A04F4) to K.S.R.M. College of Engineering (AUTONOMOUS), Kadapa in partial fulfilment of the requirements for the award of the degree of “BACHELOR OF TECHNOLOGY” in “ELECTRONICS AND COMMUNICATION ENGINEERING” is a bonafide record of the project work carried out by them under our supervision during the period 2023-2024.


Project guide

Dr. V. Adinarayana Reddy, M. Tech., Ph.D.,


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Date: 26/04/2024


Internal Examiner


External Examiner

212

K.S.R.M.COLLEGE OF ENGINEERING (AUTONOMOUS)

(Approved by AICTE, New Delhi & Affiliated to JNTUA, Ananthapuramu)

(Accredited by NAAC & NBA)

(An ISO 9001:2015 Certified Institution)


KADAPA – 516005 (A.P.)


DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING




CERTIFICATE

This is to certify that the project report entitled **“ANTI COLLISION SYSTEM USING LIDAR CUM CAMERA”**, is being submitted by **SANA YASHWANTH(209Y1A04C2), S. MOHAMMED TAYYAB(209Y1A04C9), Y. MAHESWAR REDDY(209Y1A04F8)** to **K.S.R.M. College of Engineering (AUTONOMOUS), Kadapa** in partial fulfilment of the requirements for the award of the degree of **“BACHELOR OF TECHNOLOGY”** in **“ELECTRONICS AND COMMUNICATION ENGINEERING”** is a bonafide record of the project work carried out by them under our supervision during the period **2023-2024**.

Project guide 
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